

UHF TRANSCEIVER ASSEMBLY 25W

ERM 8000

TECHNICAL MAINTENANCE MANUAL

VOLUME 2

2/2 DIAGRAMS

ERM 8000

LIST OF MODIFICATIONS

FEBRUARY 1980

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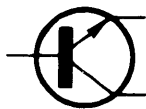
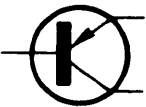
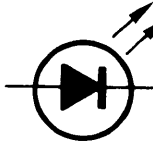
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DEFINITIONS OF SYMBOLS USED

	Resistor
	Capacitor
	Polarized electrolytic capacitor
	Inductor
	Iron-cored inductor

SEMICONDUCTORS

	NPN transistor		Voltage-regulating diode
	PNP transistor		Light-emitting diode

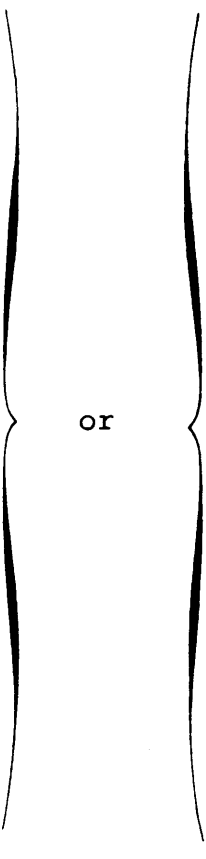
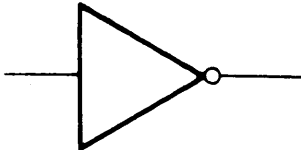
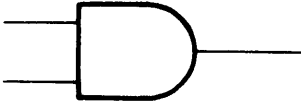
ANALOGUE OPERATORS

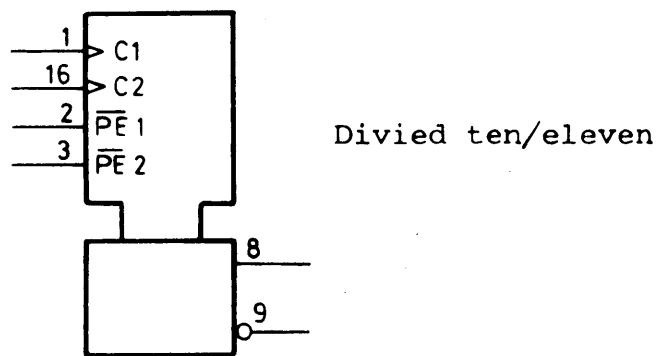
	Amplifier		Varicap tuned circuit
	Variable-gain amplifier		Band-pass filter
	Mixer		Low-pass filter
	High-frequency (HF) generator		High-pass filter

ANALOGUE OPERATORS (Continued)

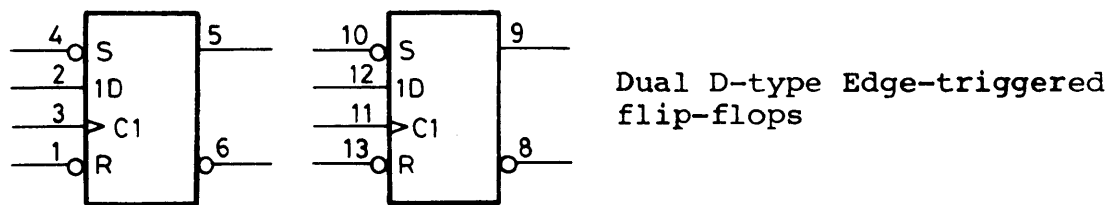
	Frequency discriminator		Switch
	Doubler		Variable attenuator
	Low-frequency (AF) generator		Square-wave generator
	Detector		Shaping

LOGICAL OPERATORS

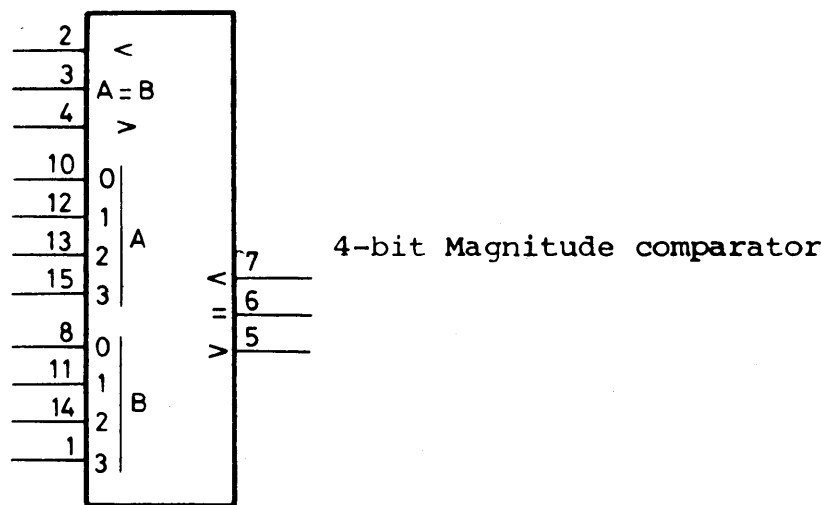
	Inverter			Inverter
	AND gate			AND gate
	NAND gate			NAND gate
	OR gate			OR gate
	NOR gate			NOR gate



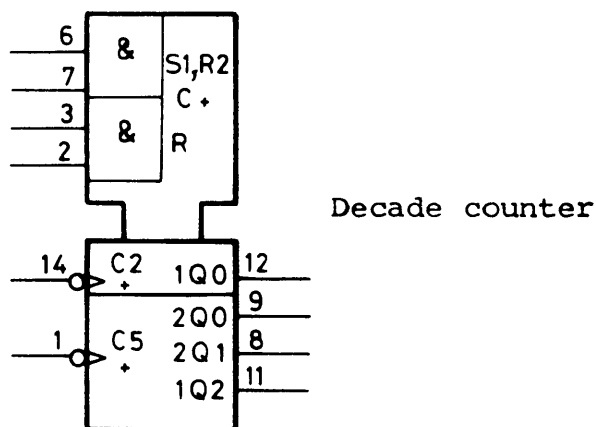
Divided ten/eleven



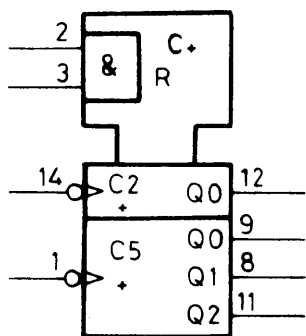
Dual D-type Edge-triggered flip-flops



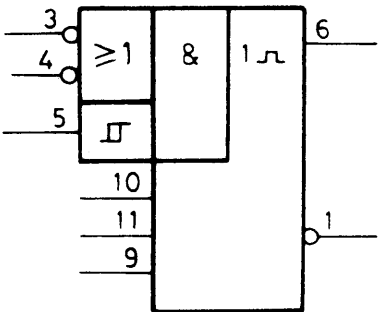
4-bit Magnitude comparator



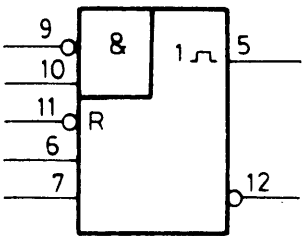
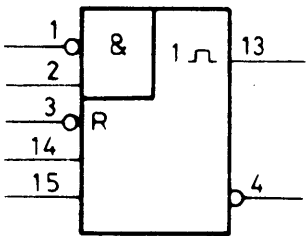
Decade counter



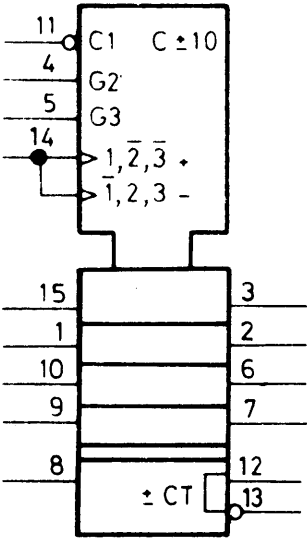
4-bit Binary counter



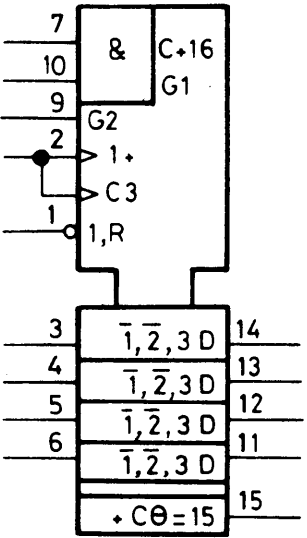
Monostable multivibrator



Retriggerable monostable multivibrator with clear



Synchronous UP/DOWN counter with
DOWN/UP mode control

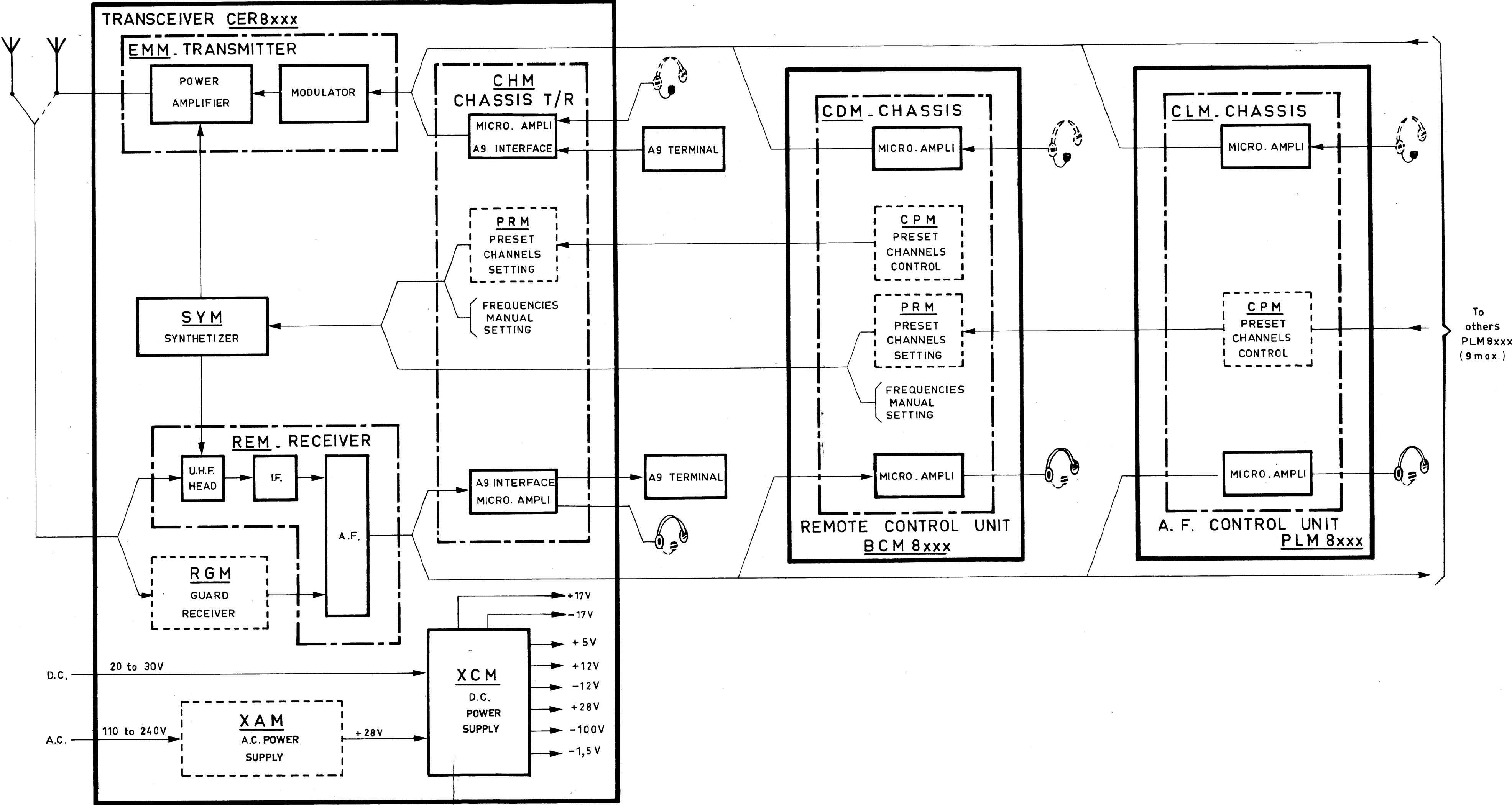


Synchronous 4-bits counter

TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
BLOCK DIAGRAM

Text reference :Chap.1-61-1-3

DIAGRAM :1.1



**TRANSMITTER - RECEIVER
SYSTEM
ERM 8000**

BLOCK DIAGRAM

SYNTHESIZER MODULE - SYM

Text reference :Chap.1- 61-2-1

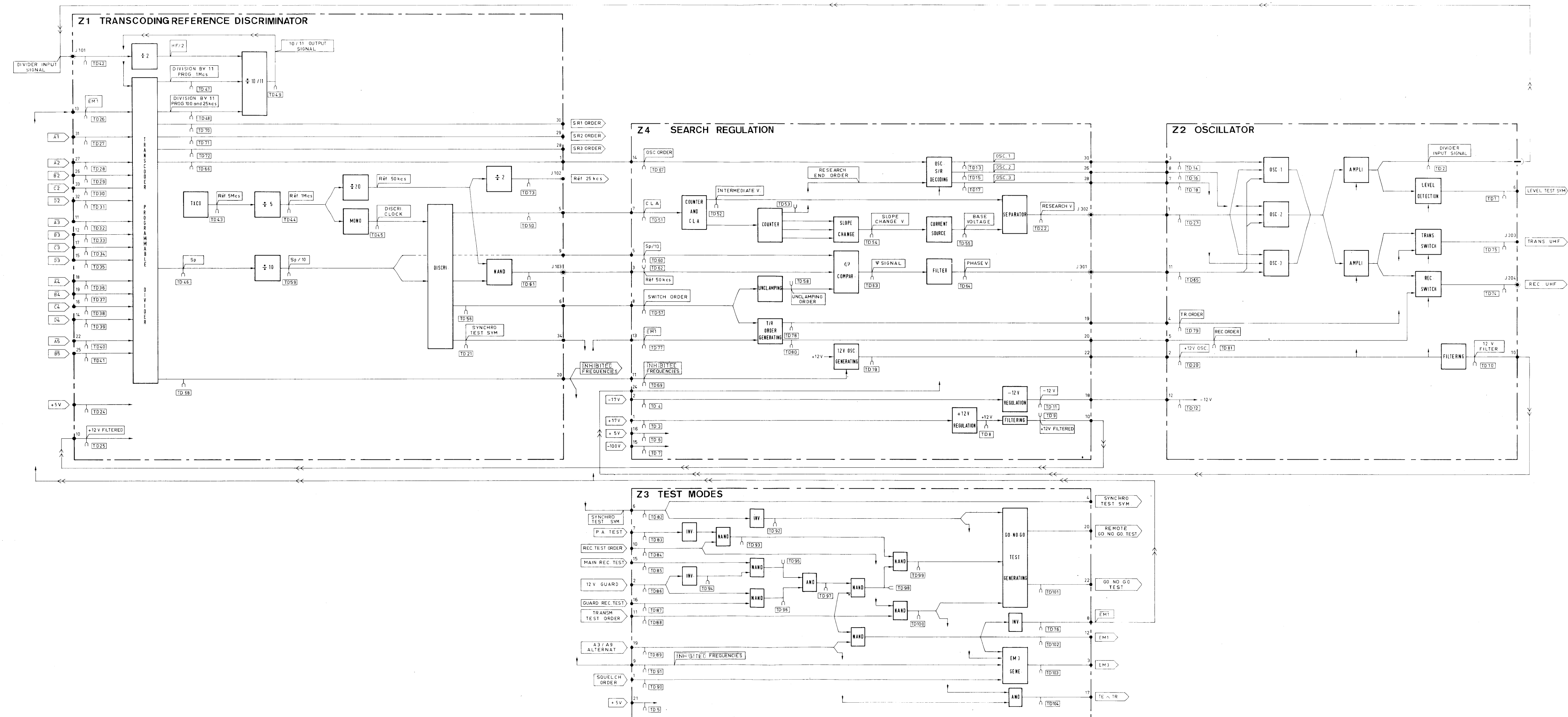


DIAGRAM :1.2

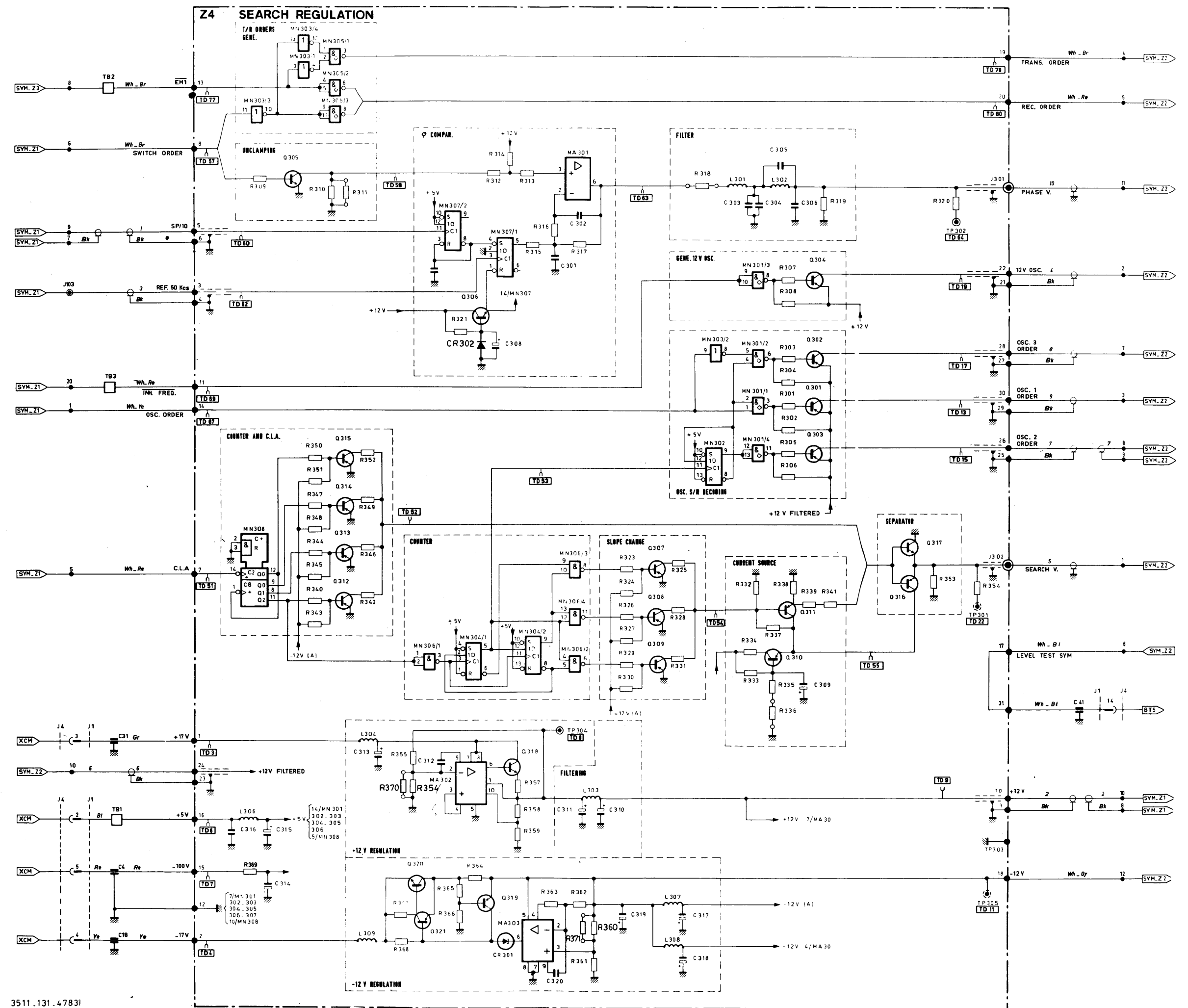
Text reference : Chap.1- § 1-2-1-4



TRANSMITTER - RECEIVER SYSTEM ERM 8000 CIRCUIT DIAGRAM SYNTHESIZER MODULE - SYM SEARCH - REGULATION CARD - Z4

Text reference :Chap.1-61-2-1-5

DIAGRAM : 1.4



TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
SYNTHESIZER MODULE - SYM
OSCILLATOR - Z2

Text reference :Chap.1-61-2-1-6

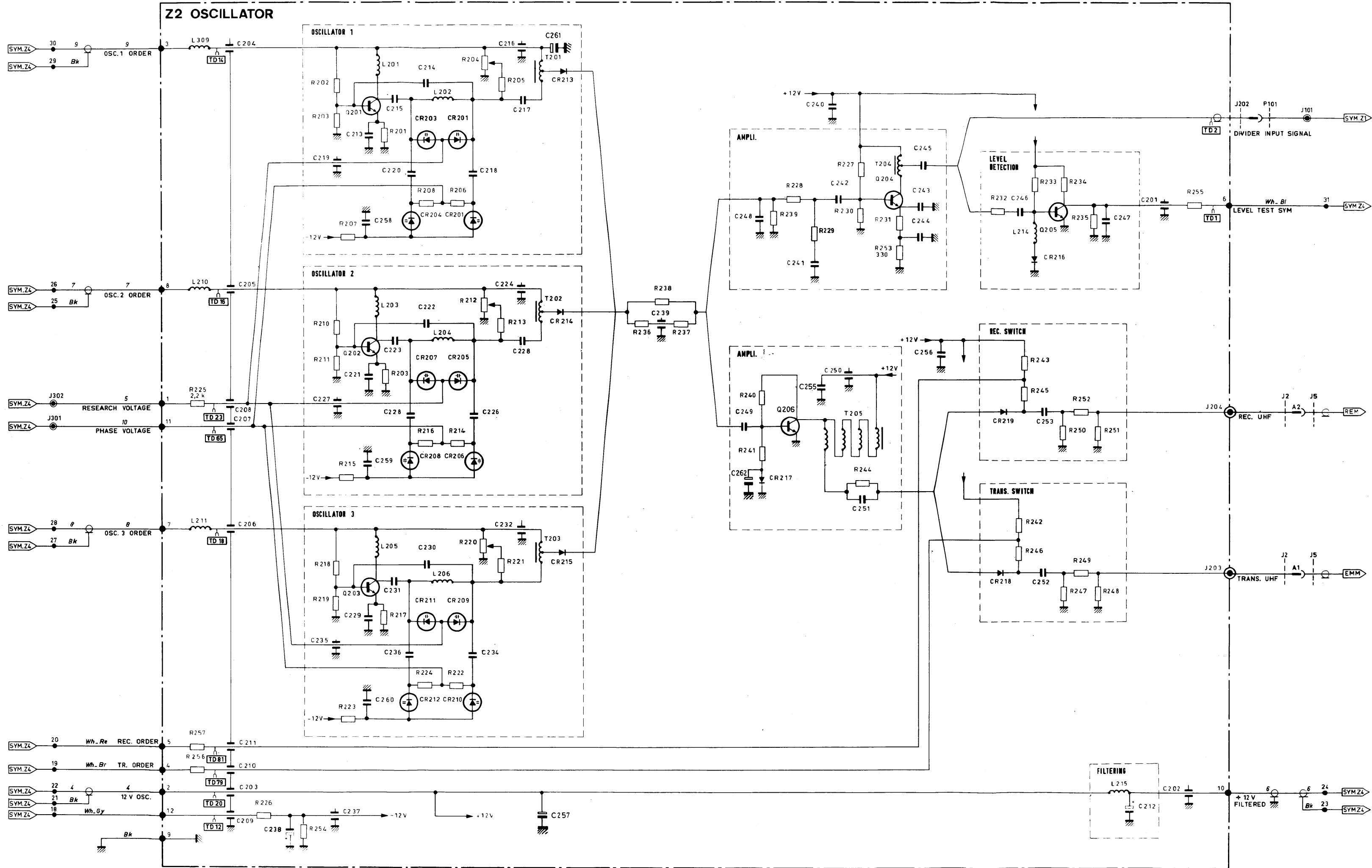
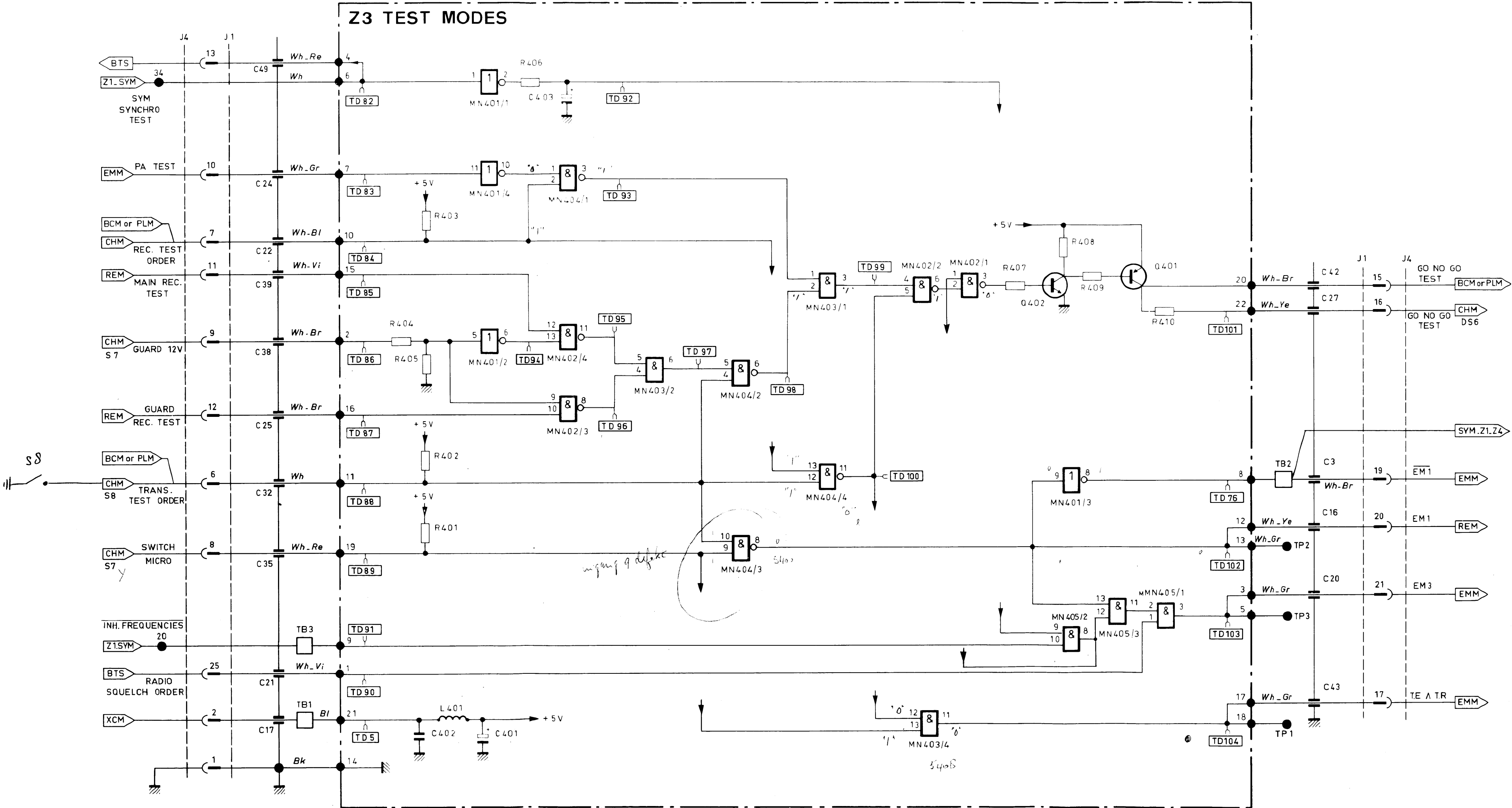


DIAGRAM: 1.5

TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
SYNTHESIZER MODULE - SYM
TEST-MODE CARD - Z3

Text reference :Chap.1- 61-2-1-7

DIAGRAM : 1.6



TRANSMITTER - RECEIVER SYSTEM ERM 8000

BLOCK DIAGRAM

TRANSMITTER MODULE-EMM

A3/A9 MODULATOR

Text reference :Chap.1- §1-2-2-3-A

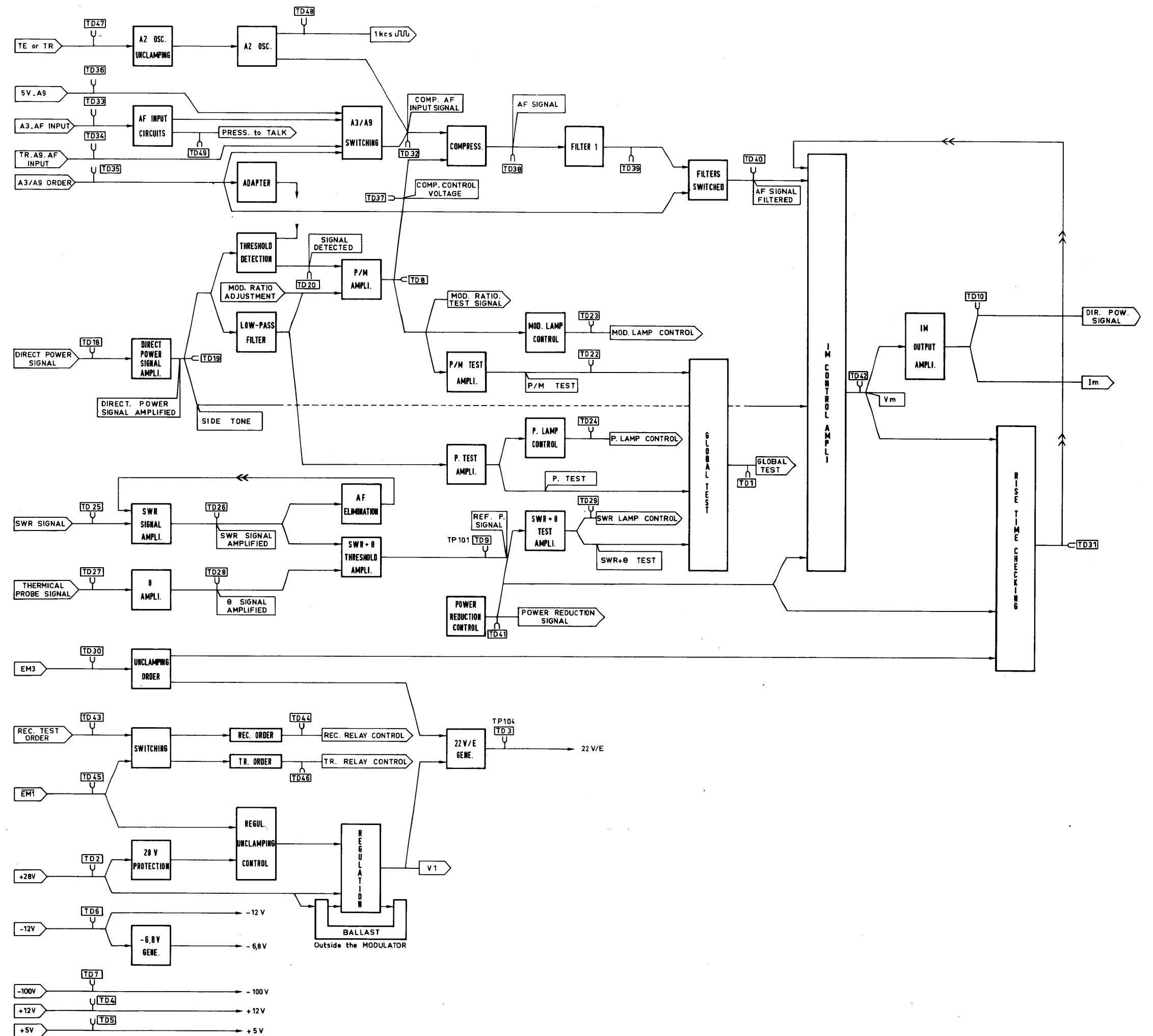


DIAGRAM:1.7

**TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
TRANSMITTER MODULE - EMM
A3/A9 MODULATOR**

Text reference :Chap 1-6 1-2-2-3-A

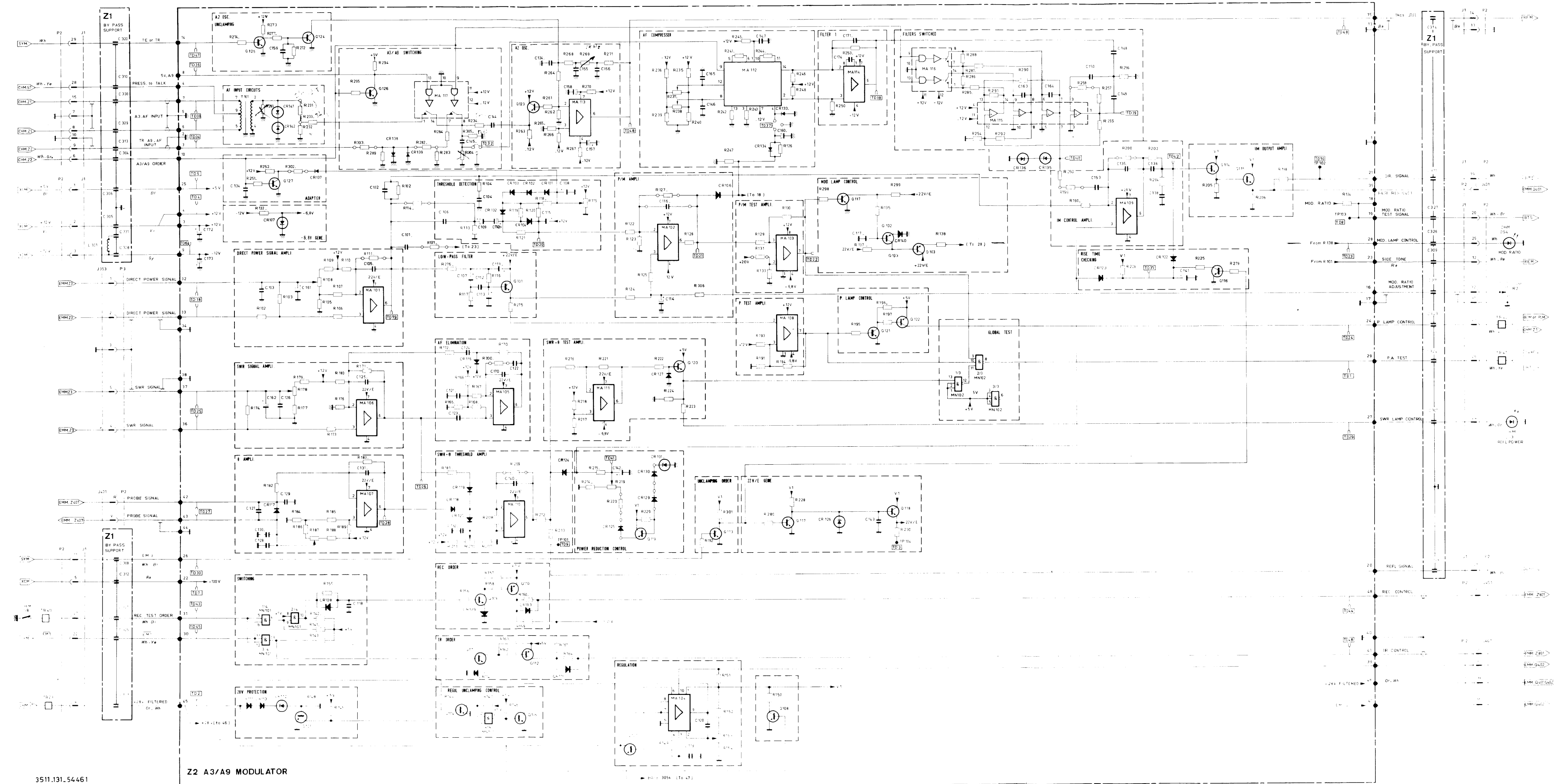


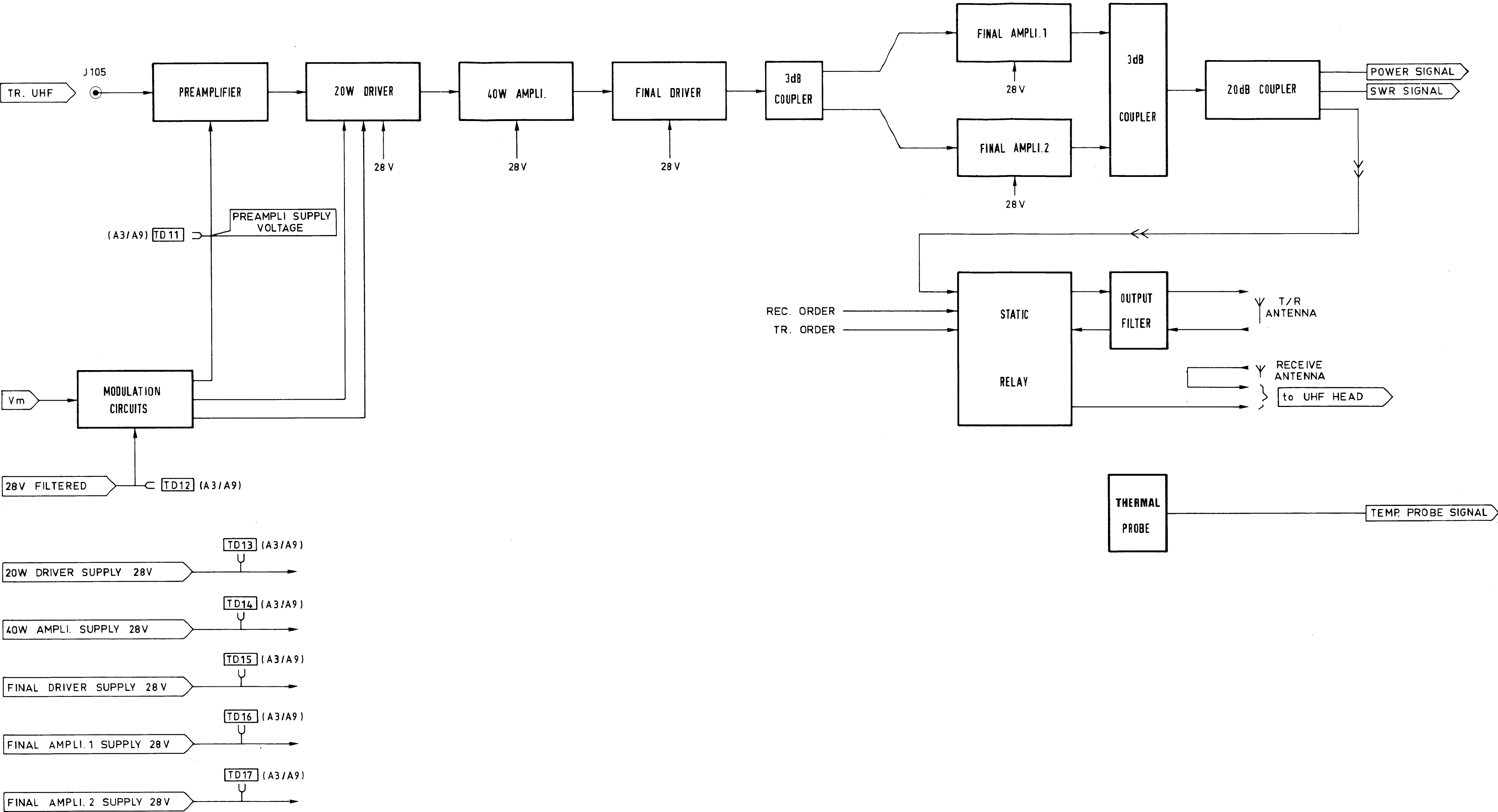
DIAGRAM : 1.8

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IND.0:79.06.11
MODULATFOR A3.2.2 P

TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
BLOCK DIAGRAM
TRANSMITTER MODULE-EMM
UHF AMPLIFICATION

Text reference :Chap.1-61-2-2-3-B

DIAGRAM:1.9



TRANSMITTER - RECEIVER SYSTEM ERM 8000

CIRCUIT DIAGRAM

TRANSMITTER MODULE - EMM

POWER AMPLIFIER (1

Text reference :Chap.1-61-2-2-3-1

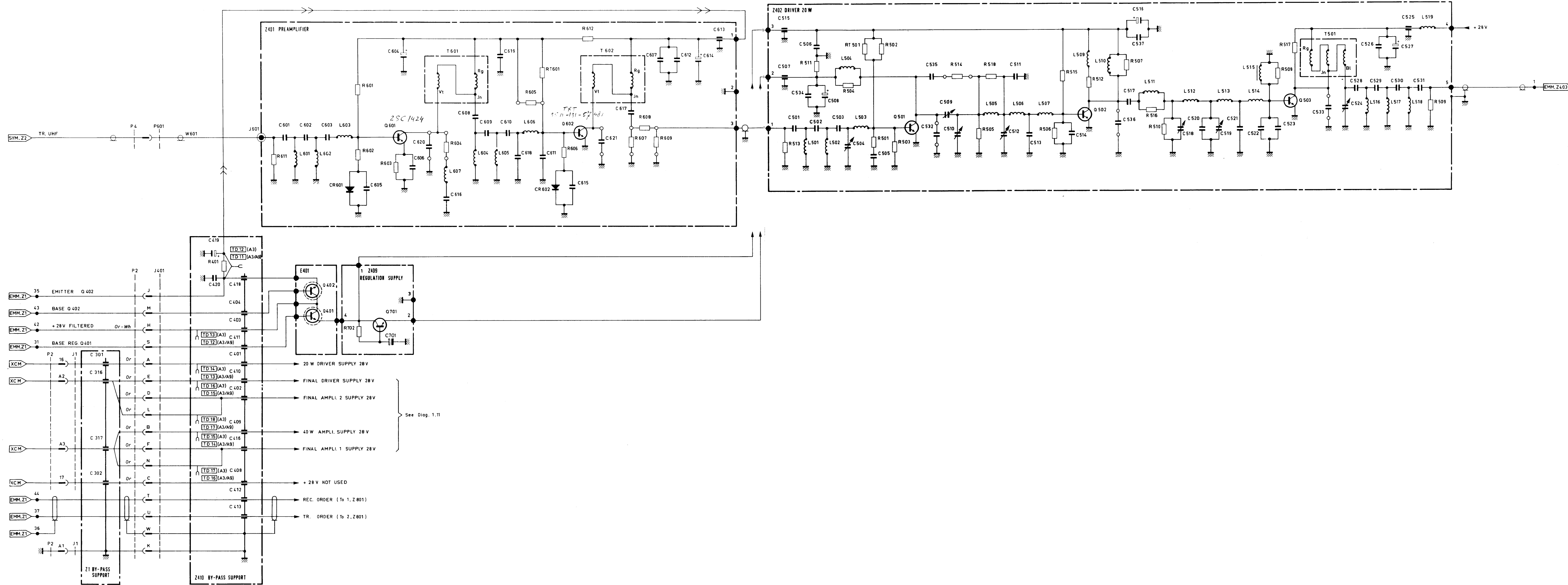


DIAGRAM: 1.1

TRANSMITTER - RECEIVER SYSTEM

ERM 8000

CIRCUIT DIAGRAM

TRANSMITTER MODULE - EMM

POWER AMPLIFIER (2)

Text reference : Chap.1- 61-2-2-3-B

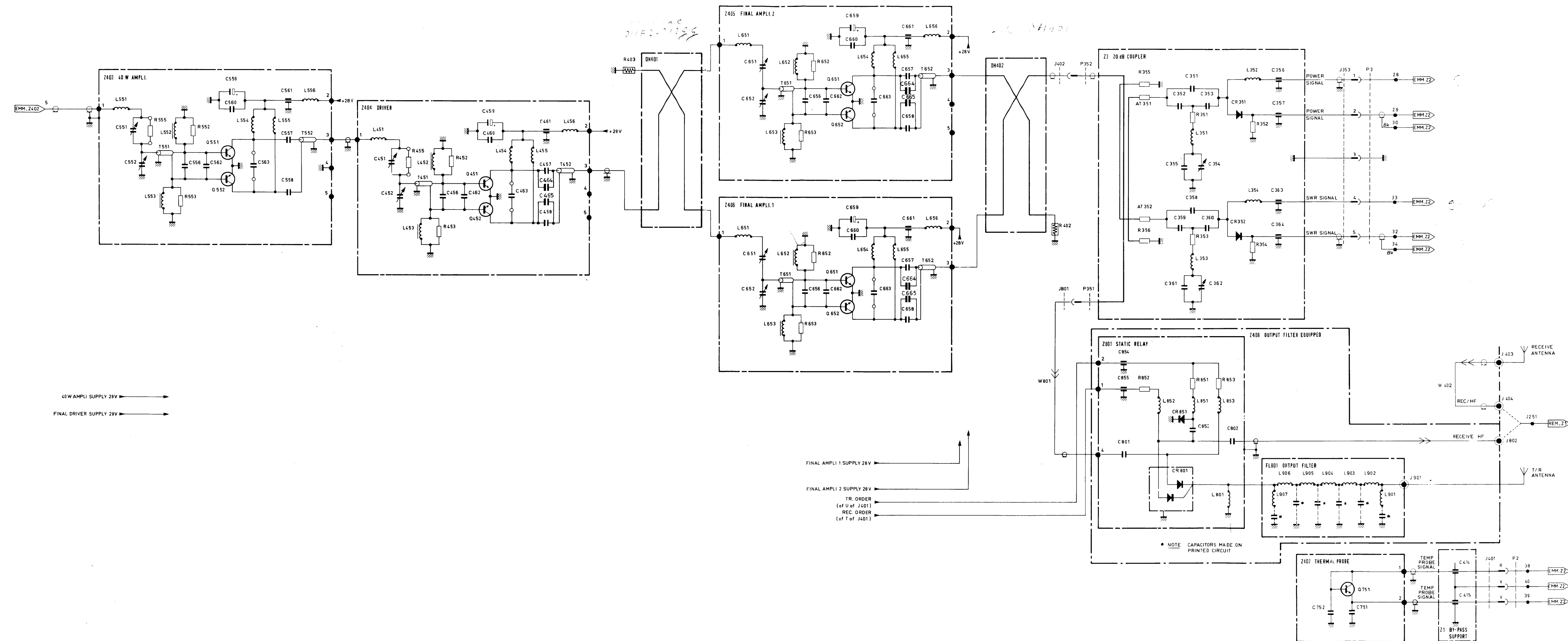


DIAGRAM: 1.11

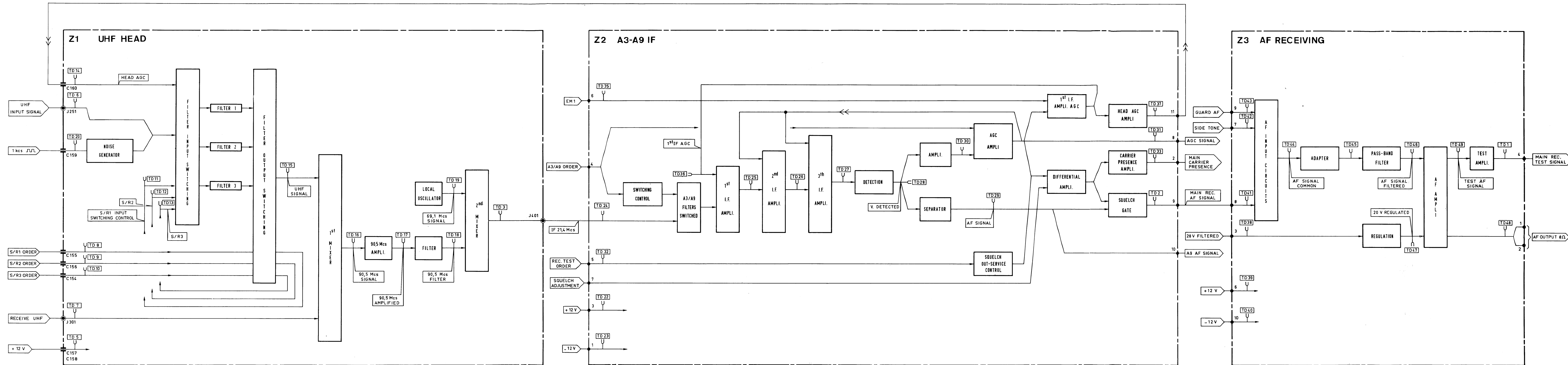
3511 131 48992
IND.D:80.09.0

RADIATEUR CABLE

TRANSMITTER - RECEIVER SYSTEM ERM 8000

BLOCK DIAGRAM A3/A9 RECEIVER MODULE - REM

Text reference :Chap.1-§12-3

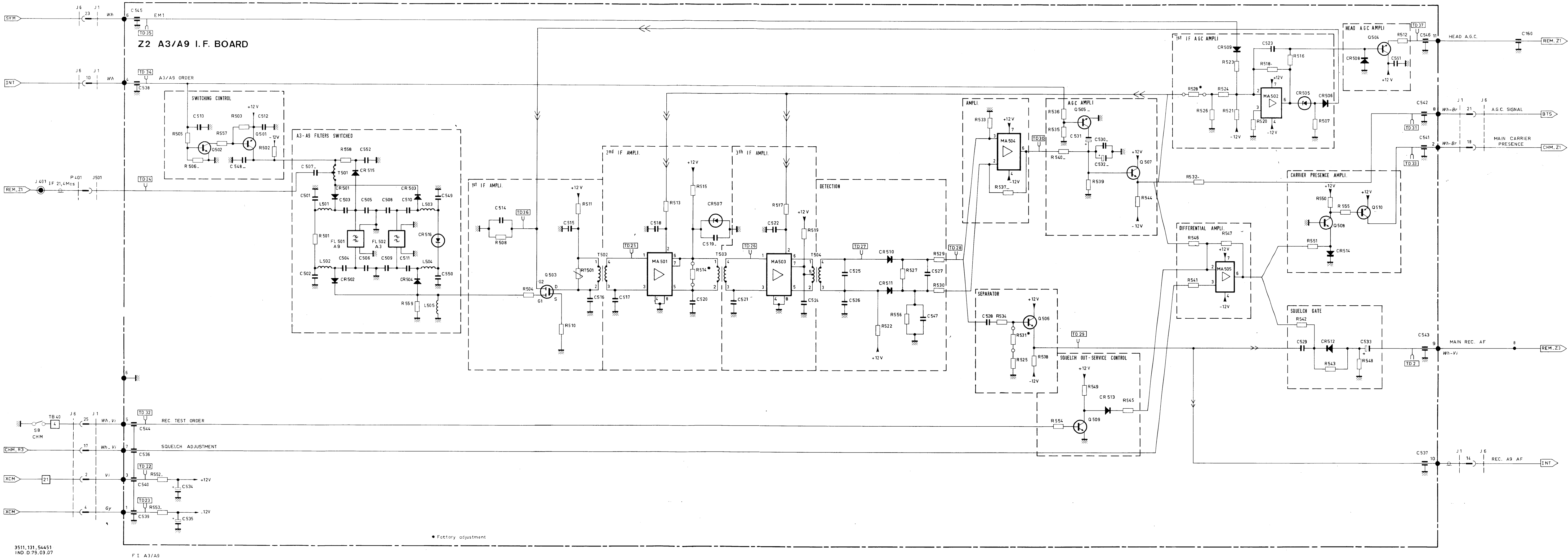


TRANSMITTER - RECEIVER
SYSTEM
ERM 8000

CIRCUIT DIAGRAM
RECEIVER MODULE - REM
IF CARD A3/A9 MODE
Z2

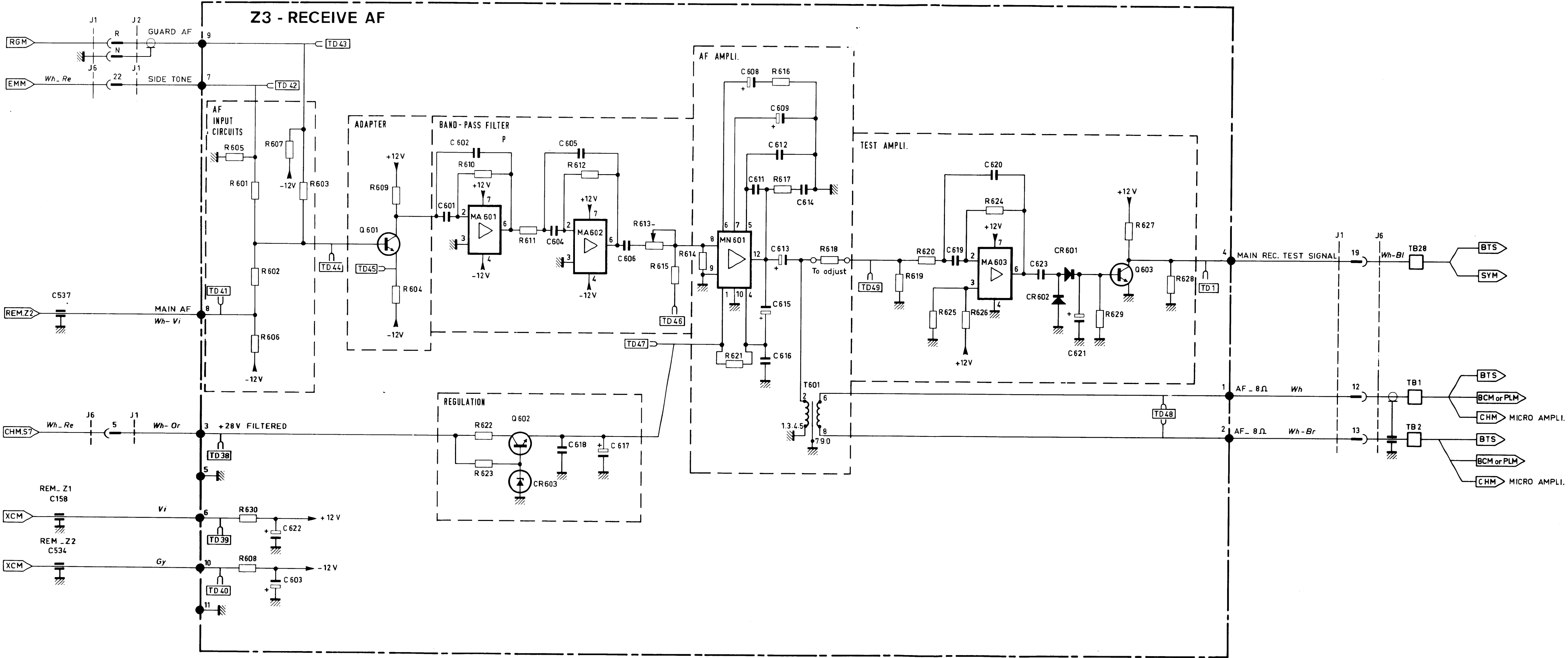
Text reference :Chap.1-61-2-3-3-B

DIAGRAM:1.14



TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
RECEIVER MODULE - REM
AF AMPLIFIER CARD
Z3

Text reference : Chap.1-§1-2-3-3-C



BF RECEPTION

DIAGRAM : 1.15

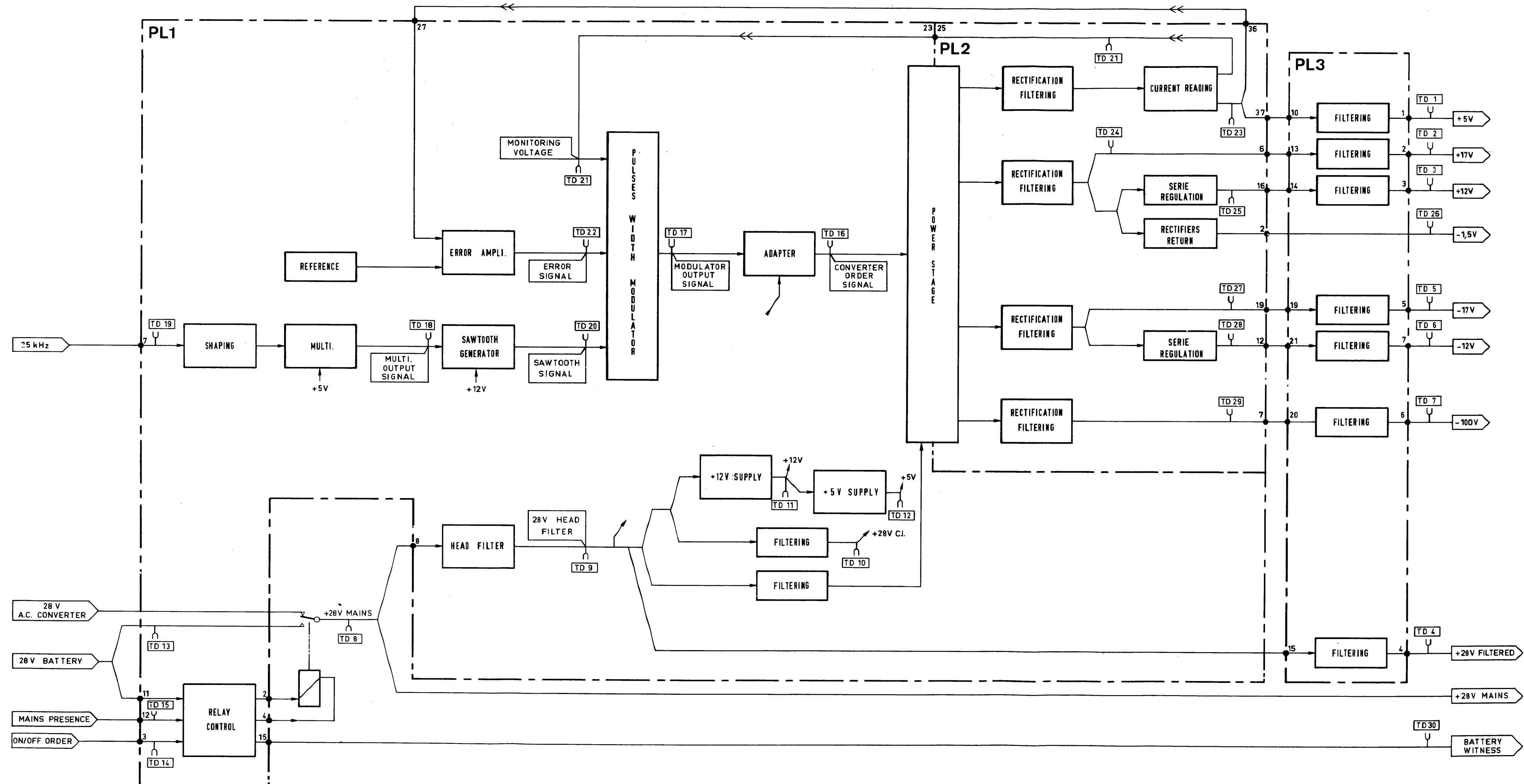
3511_131_46131
IND. D: 81.02.23

TRANSMITTER - RECEIVER SYSTEM ERM 8000

BLOCK DIAGRAM
POWER SUPPLY 24V DC
XCM

Text reference :Chap.1-61-2-4

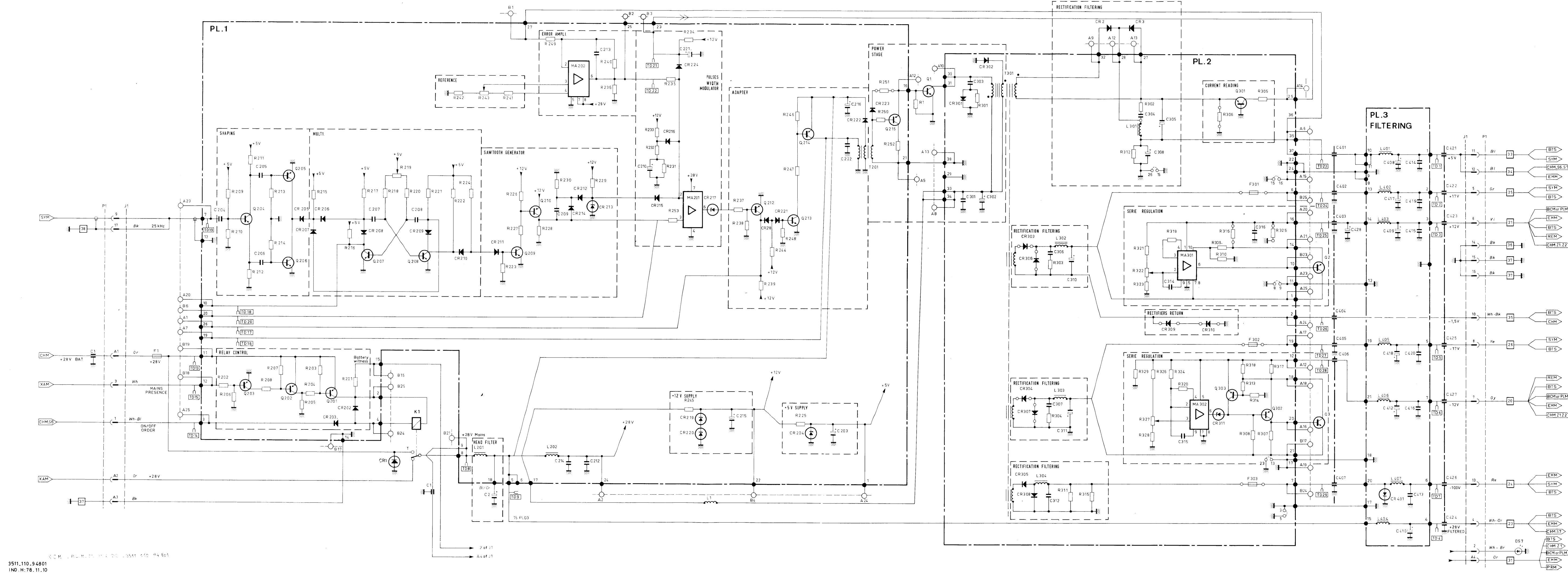
DIAGRAM: 1.16



TRANSMITTER - RECEIVER SYSTEM ERM 8000 CIRCUIT DIAGRAM POWER SUPPLY 24V DC XCM

Text reference :Chap.1-61-2-4

DIAGRAM : 1.17



XCM L.P.E. 110.94601
3511.110.94601
IND. H: 78.11.10

**TRANSMITTER - RECEIVER
SYSTEM
ERM 8000**

CIRCUIT DIAGRAM

CHASSIS TRANSCEIVER - CHM

INTERCONNECTIONS

Text reference :Chap.1-61-2-5

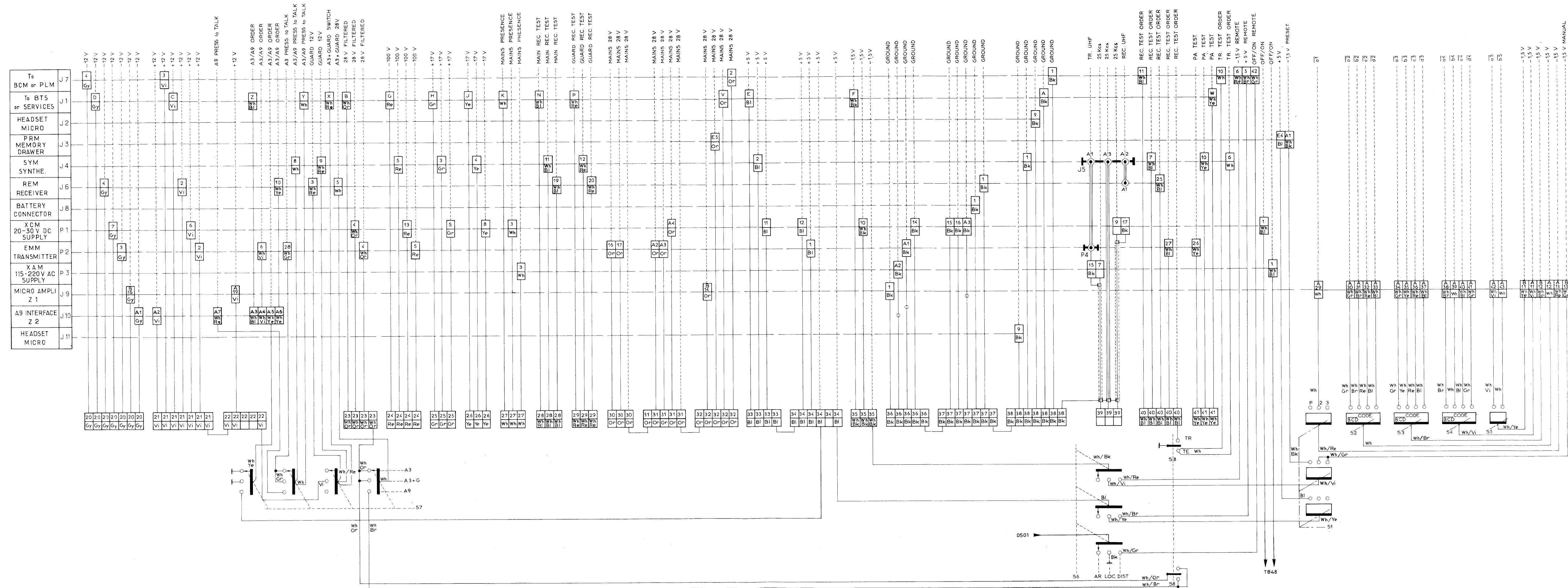
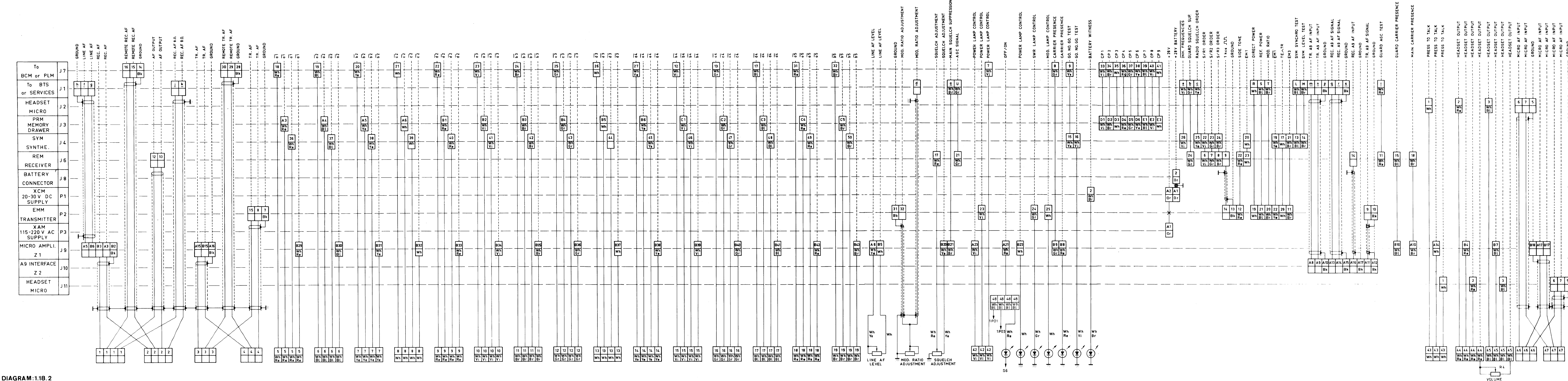


DIAGRAM: 1.18.1

TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
CHASSIS TRANSCEIVER - CHM
INTERCONNECTIONS

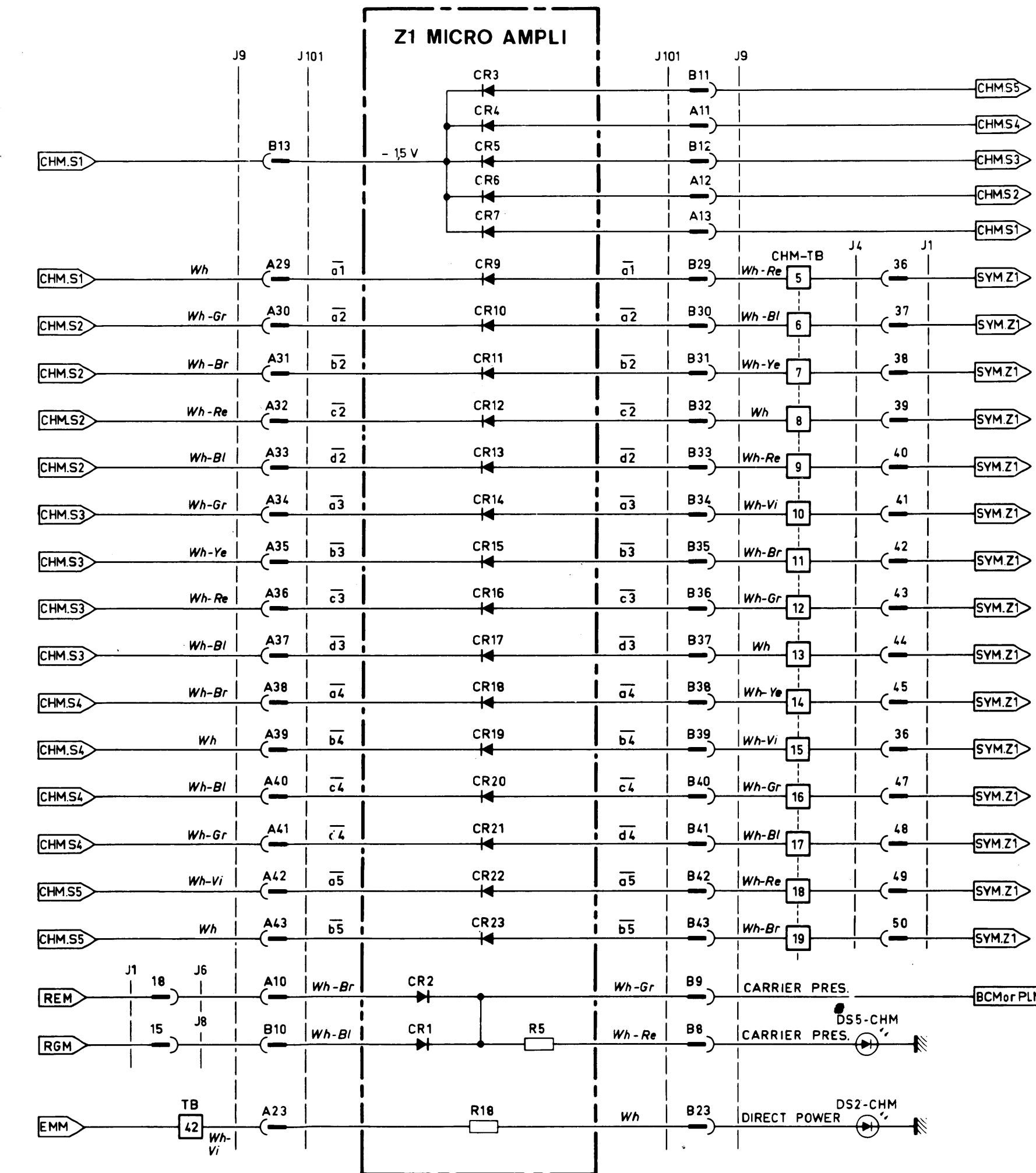
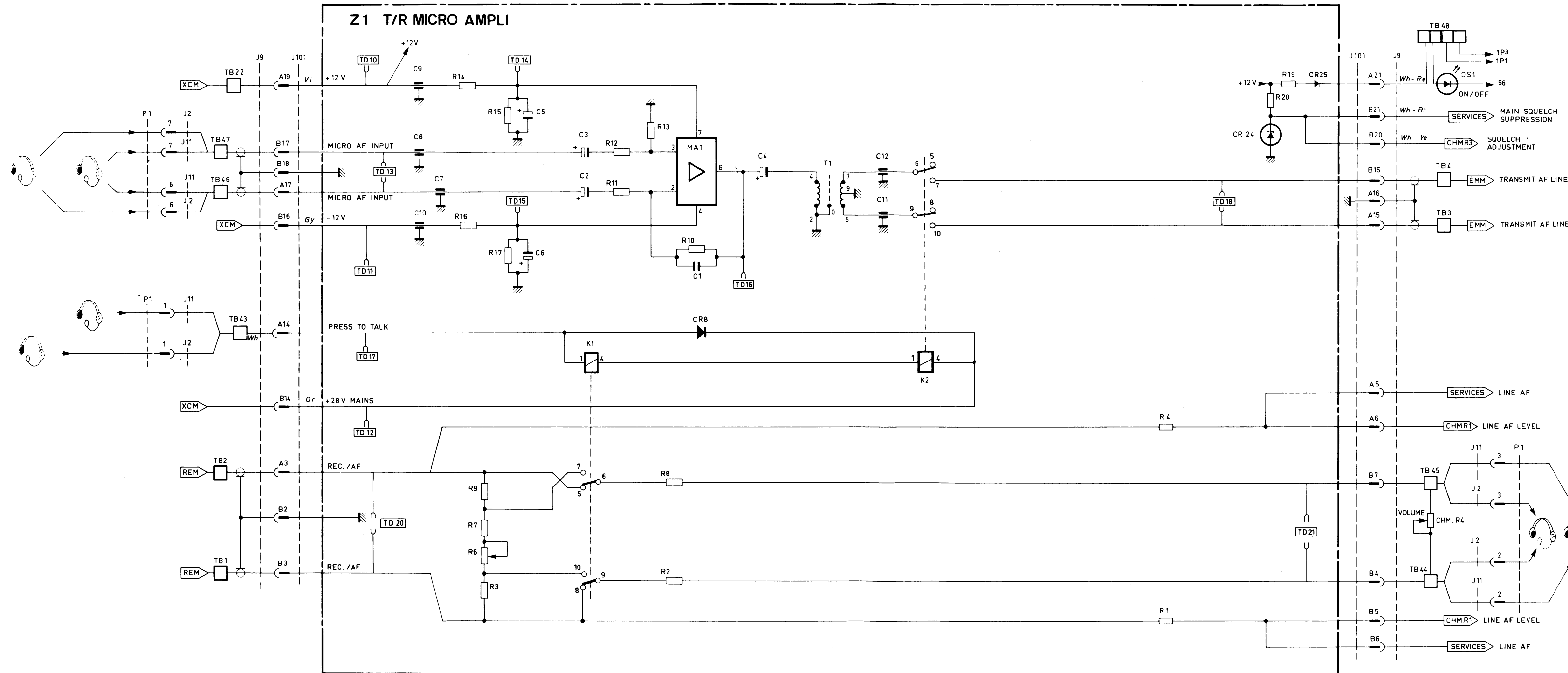
Text reference :Chap.1-§1-2-5



TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
CHASSIS TRANSCEIVER - CHM
MICRO AMPLI Z1

Text reference :Chap.1-61-2-5-2

DIAGRAM : 1.19

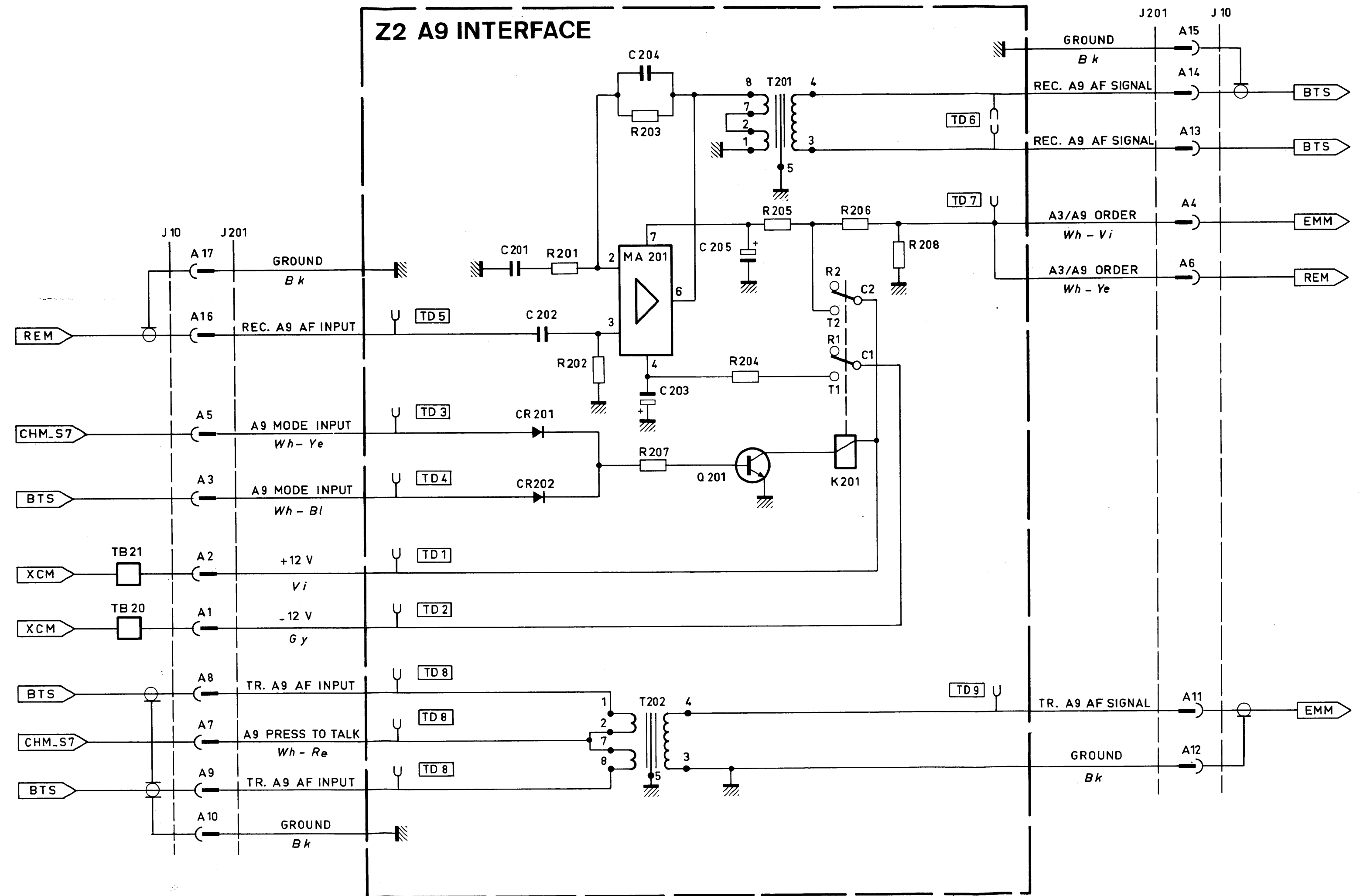


TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
CHASSIS TRANSCEIVER - CHM
MICRO AMPLIFIER
A9 INTERFACE - Z2

Text reference : Chap.1- §1-2-5-3

DIAGRAM : 1.20

3511.131 - 54471



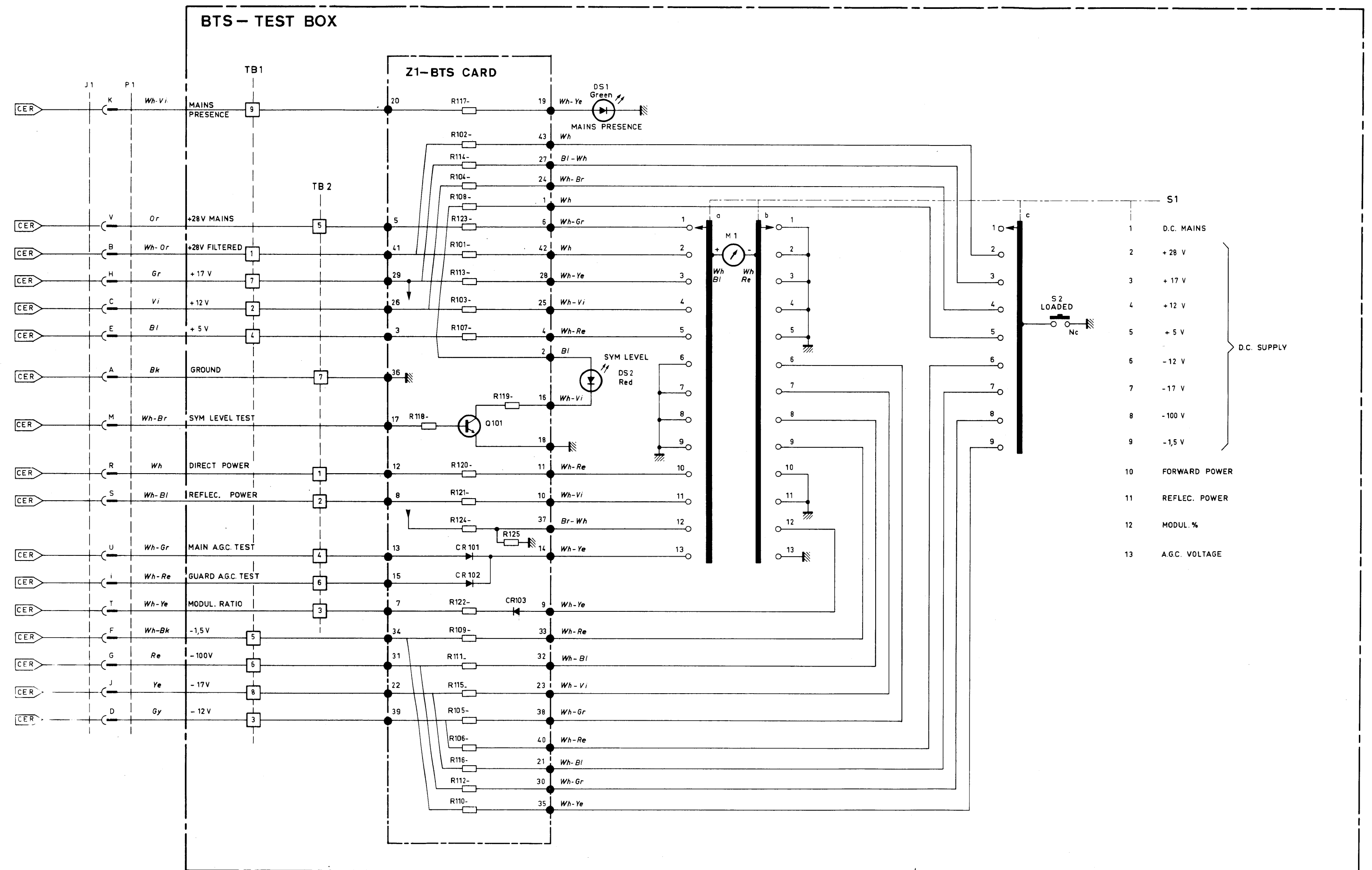
TRANSMITTER - RECEIVER SYSTEM ERM 8000 CIRCUIT DIAGRAM TEST BOX BTS

Text reference :Chap.1-§1-2-6

DIAGRAM: 1.21

3511.131-283 81
IND. E:81.07.28

DATE OF TEST



**TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
MEMORY DRAWER
PRM**

Text reference : Chap.1- 6 1-2-7

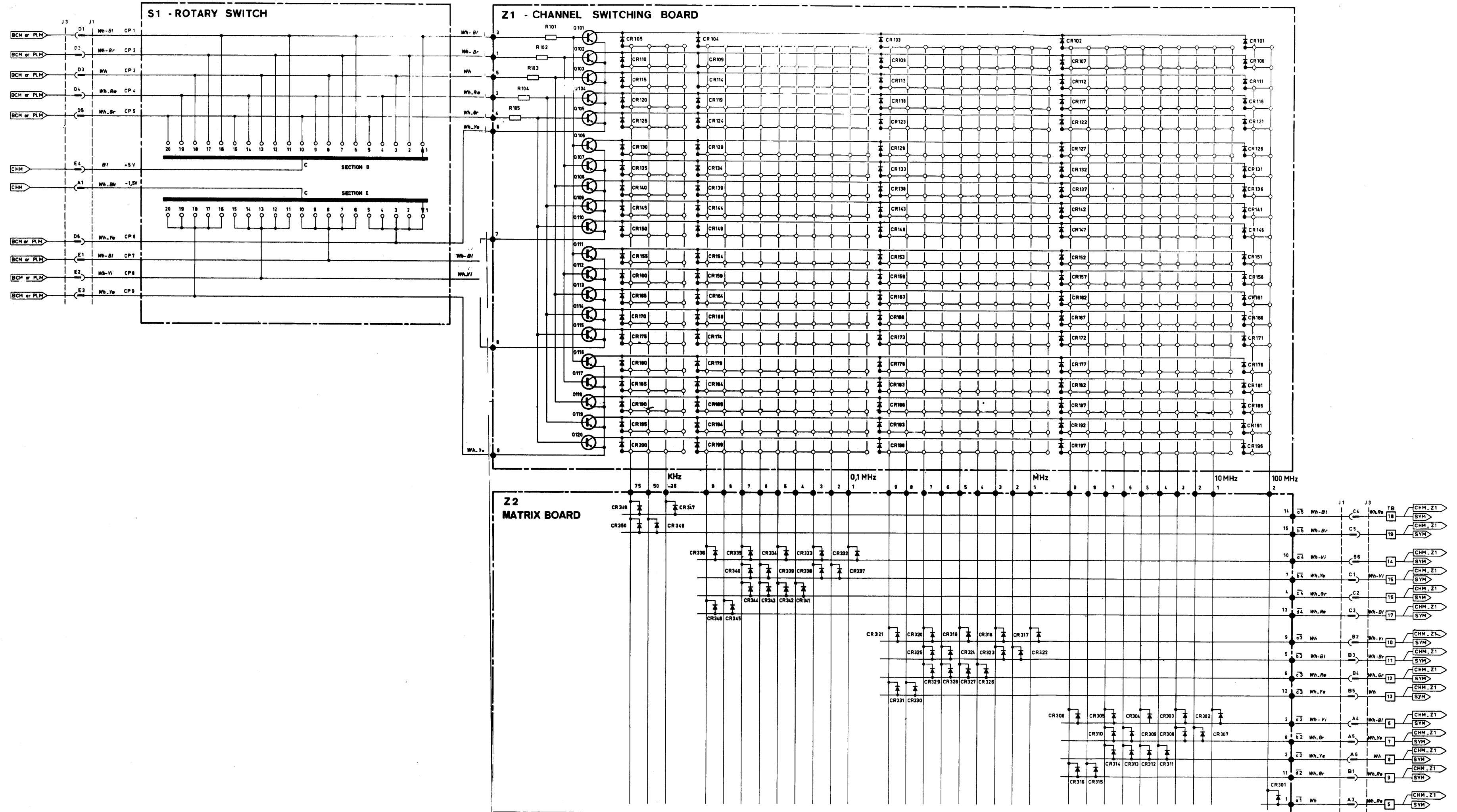


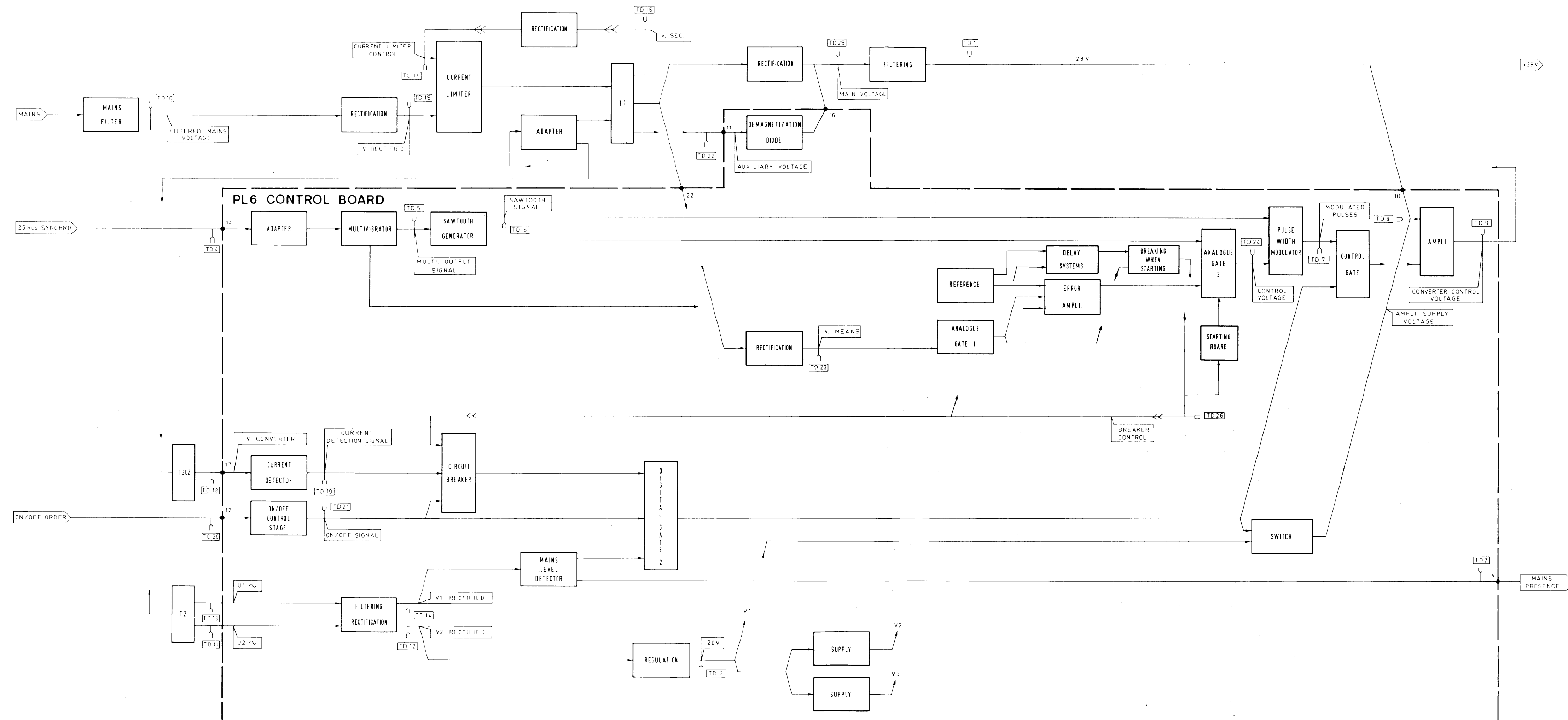
DIAGRAM: 1.22

BLOCK DIAGRAM

POWER SUPPLY 115/220V AC

XAM

DIAGRAM:1.23



**TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
POWER SUPPLY 115/220V AC
XAM**

Text reference : Chap.1- 61-2-8

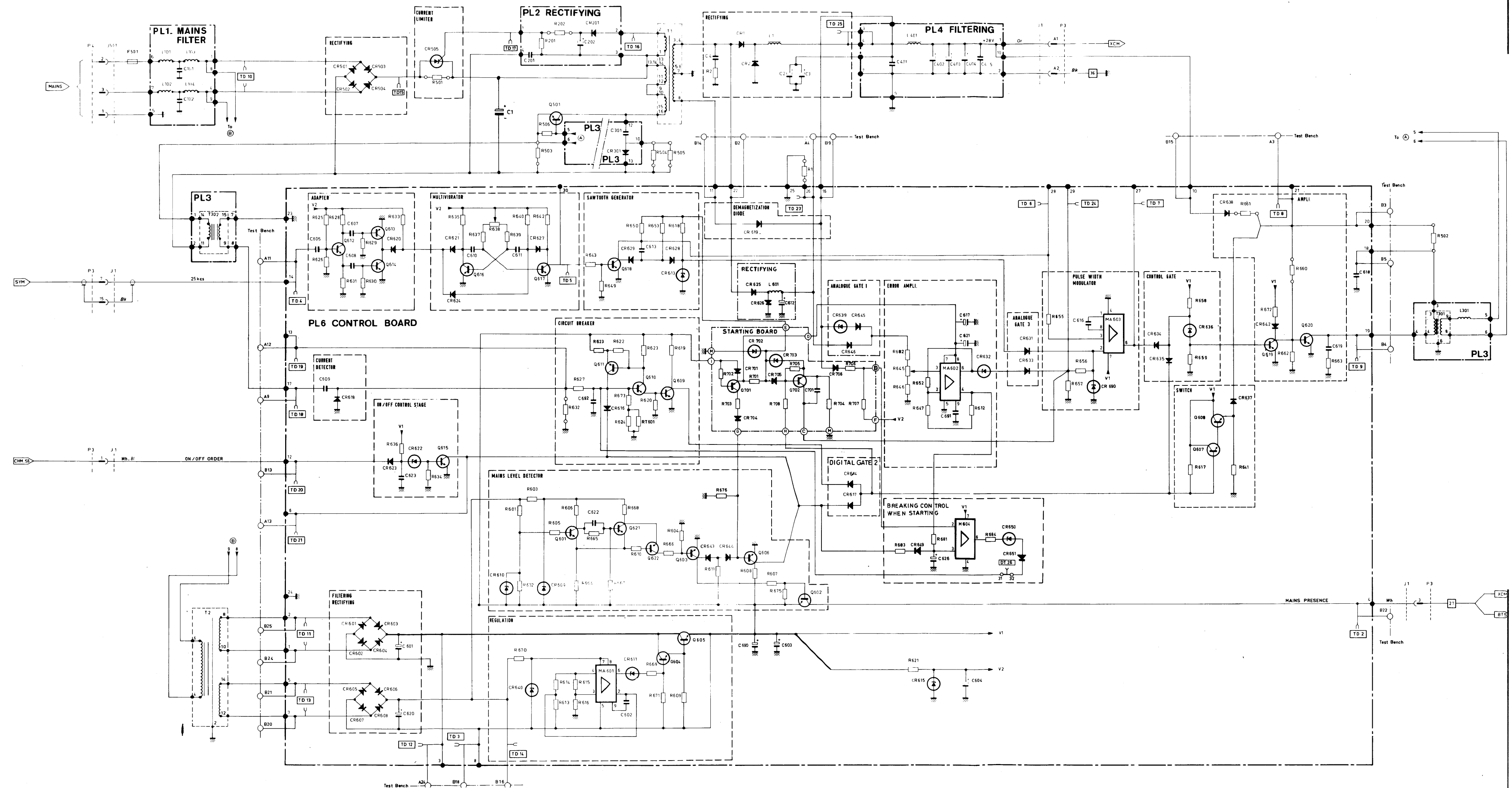


DIAGRAM: 1.24

TRANSMITTER - RECEIVER
SYSTEM
ERM 8000

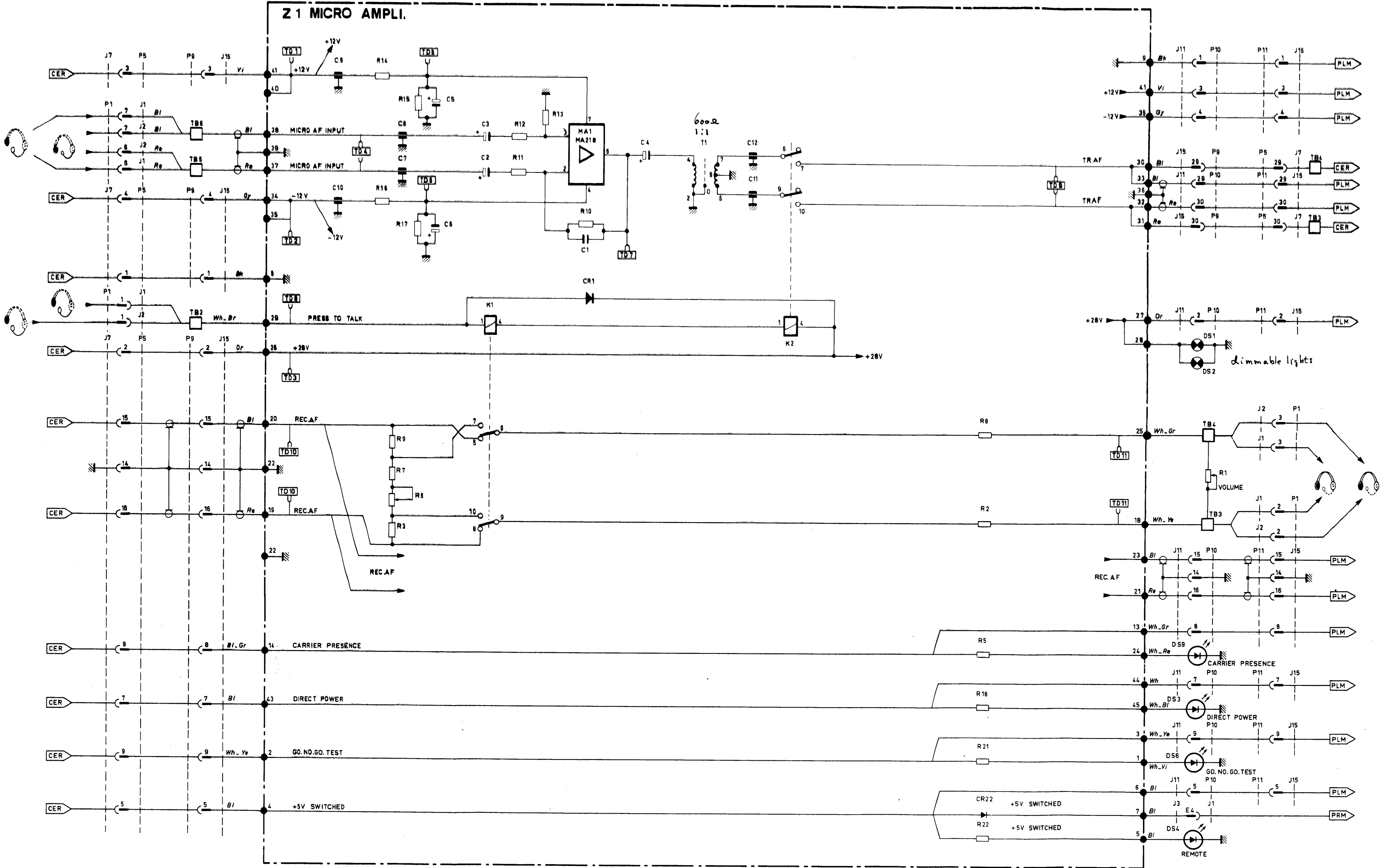
CIRCUIT DIAGRAM

AF CONTROL UNIT - PLM

Text reference :Chap.1-61-2-11

DIAGRAM:1.25

3511.131.55261



TRANSMITTER - RECEIVER
SYSTEM
ERM 8000
CIRCUIT DIAGRAM
PRESET CHANNEL CONTROL
UNIT - CPM

Text reference :Chap.1- 91-2-12

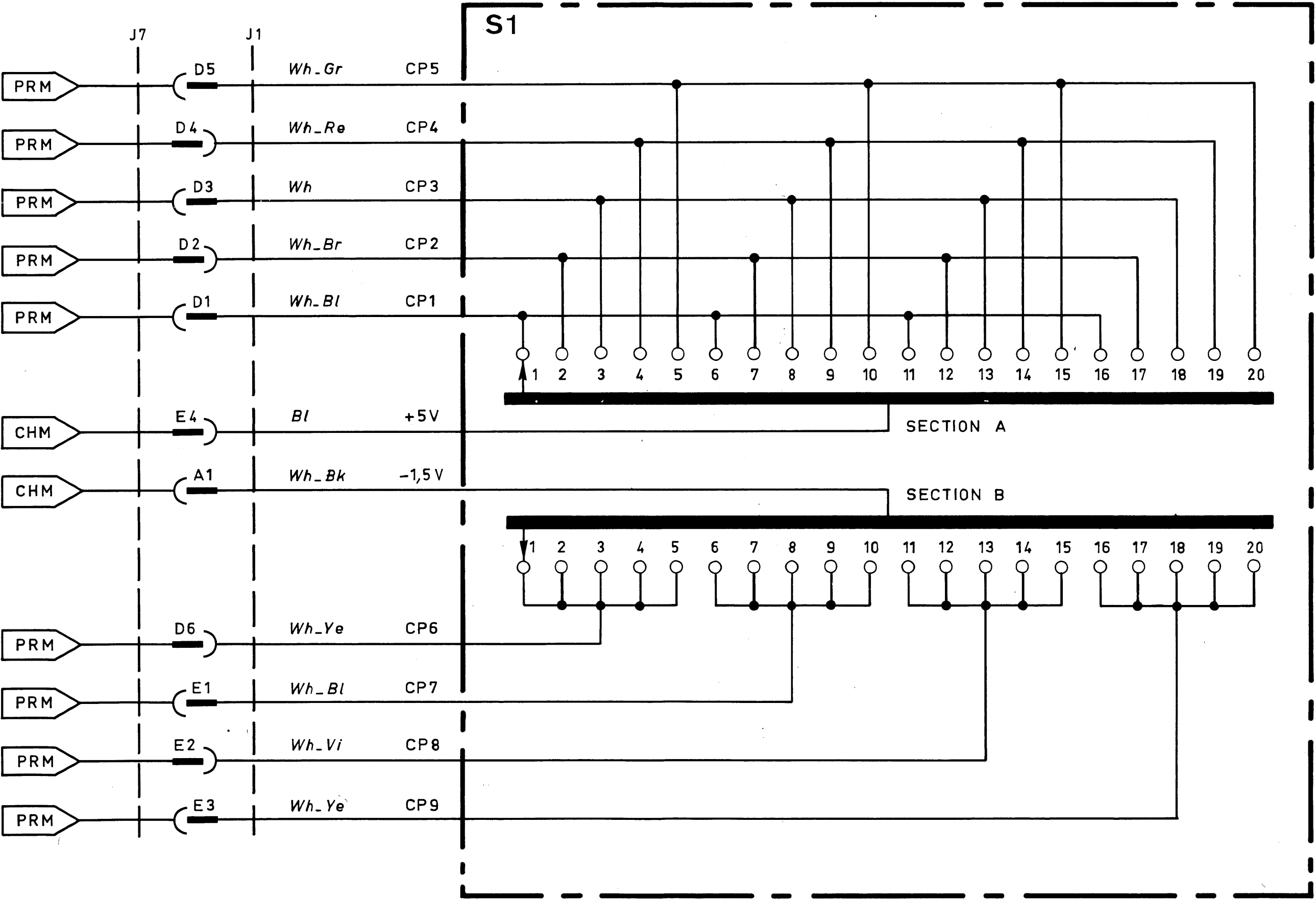


DIAGRAM:1.26

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